

3.9-04

L Number	Hits	Search Text	DB	Time stamp
1	21	chan-darin-a.in.	USPAT; US-PGPUB; EPO; JPO;	2004/03/09 16:13
2	0	chan-darin-a.in. and second adj transistor	IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM TDB	2004/03/09 16:13
3	14	chan-darin-a.in. and transistor	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2004/03/09 16:14
4	1	(chan-darin-a.in. and transistor) and source and drain and (silicon adj layer)	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2004/03/09 16:14
5	33	(US-6100124-\$ or US-5937285-\$ or US-5908309-\$ or US-5015595-\$ or US-5273914-\$ or US-4902634-\$ or US-4422885-\$ or US-4771014-\$ or US-4477310-\$ or US-4773551-\$ or US-6501097-\$ or US-4642878-\$ or US-6501097-\$ or US-6235568-\$ or US-6440832-\$ or US-5963278-\$ or US-5899547-\$ or US-6420730-\$ or US-5834350-\$ or US-5428224-\$ or US-5162877-\$ or US-5909400-\$ or US-5162877-\$ or US-5909400-\$ or US-5463238-\$ or US-5164803-\$).did. or (US-5463238-\$ or US-5164803-\$).did. or (US-20020195623-\$ or US-20020102780-\$ or US-20020171077-\$).did.	USPAT; US-PGPUB	2004/03/09 16:15
6	7		USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/03/09 16:33
7	39	(second adj transistor) and (diffusivity)	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2004/03/09 16:34

8	39	((second adj transistor) and	USPAT;	2004/03/09
"		(diffusivity)) not (chan-darin-a.in.	US-PGPUB;	16:34
		((US-6100124-\$ or US-5937285-\$ or	EPO; JPO;	
		US-5908309-\$ or US-5015595-\$ or	IBM TDB	
		US-5273914-\$ or US-4902634-\$ or	_	
ĺ		US-4422885-\$ or US-4771014-\$ or		
		US-4477310-\$ or US-4703551-\$ or		
		US-6501097-\$ or US-4642878-\$ or		
		US-4637124-\$ or US-6235568-\$ or		
		US-6440832-\$ or US-5963278-\$ or		
		US-5899547-\$ or US-6060748-\$ or		
		US-5627399-\$ or US-6420730-\$ or		
		US-5834350-\$ or US-5428224-\$ or		
		US-5162877-\$ or US-5909400-\$ or		
]	US-6180986-\$ or US-6537891-\$).did. or		
		(US-5463238-\$ or US-5164803-\$).did. or		
		(US-20020195623-\$ or US-20010016377-\$ or US-20020130393-\$ or US-20020102780-\$ or		
		US-20020171077-\$).did.))		
9	33	(((second adj transistor) and	USPAT;	2004/03/09
	33	(((second ad) transistor, and (diffusivity)) not (chan-darin-a.in.	US-PGPUB;	16:34
		((US-6100124-\$ or US-5937285-\$ or	EPO; JPO;	
		US-5908309-\$ or US-5015595-\$ or	IBM TDB	
]		US-5273914-\$ or US-4902634-\$ or		
		US-4422885-\$ or US-4771014-\$ or		
		US-4477310-\$ or US-4703551-\$ or		
		US-6501097-\$ or US-4642878-\$ or		
		US-4637124-\$ or US-6235568-\$ or		
		US-6440832-\$ or US-5963278-\$ or		
		US-5899547-\$ or US-6060748-\$ or		
		US-5627399-\$ or US-6420730-\$ or		
		US-5834350-\$ or US-5428224-\$ or		
		US-5162877-\$ or US-5909400-\$ or US-6180986-\$ or US-6537891-\$).did. or		
		(US-5463238-\$ or US-5164803-\$).did. or		
		(US-20020195623-\$ or US-20010016377-\$ or		
		US-20020130393-\$ or US-20020102780-\$ or		
		US-20020171077-\$).did.))) and (source and		
		drain)		
10	17	((((second adj transistor) and	USPAT;	2004/03/09
		(diffusivity)) not (chan-darin-a.in.	US-PGPUB;	16:34
		((US-6100124-\$ or US-5937285-\$ or	EPO; JPO;	
		US-5908309-\$ or US-5015595-\$ or	IBM_TDB	
		US-5273914-\$ or US-4902634-\$ or		
		US-4422885-\$ or US-4771014-\$ or		
		US-4477310-\$ or US-4703551-\$ or US-6501097-\$ or US-4642878-\$ or		
1		US-4637124-\$ or US-4642878-\$ or US-4637124-\$		
		US-6440832-\$ or US-5963278-\$ or		
		US-5899547-\$ or US-6060748-\$ or		
		US-5627399-\$ or US-6420730-\$ or		
		US-5834350-\$ or US-5428224-\$ or		
}		US-5162877-\$ or US-5909400-\$ or		
		US-6180986-\$ or US-6537891-\$).did. or		
]		(US-5463238-\$ or US-5164803-\$).did. or		
		(US-20020195623-\$ or US-20010016377-\$ or		
1		US-20020130393-\$ or US-20020102780-\$ or		
}		US-20020171077-\$).did.))) and (source and		
	5	drain)) and (silicon adj layer)	USPAT;	2003/08/26
-	5	(("6100124") or ("6153443") or ("6235568") or ("5940319") or	US-PGPUB;	11:04
		("5937285")).PN.	EPO; JPO;	11.03
		(5551265 1.ER.	IBM TDB	
-	9	("5089441" "5168072" "5252501"	USPAT	2003/01/30
		"5397909" "5403434" "5716861"		19:19
	İ	"5801078" "5908309" "6066523").PN.		
-	2	("5273914" "5290712").PN.	USPAT	2003/01/30
			l	19:21

_	13	("4366613" "4422885" "4477310"	USPAT	2003/01/30
		"4703551" "4771014" "4786609"	001111	19:23
		"4902634" "4975718" "4987089"		
		"5015595" "5030582" "5132234"		
_	1	"5141890").PN. "6501097"	USPAT;	2003/01/30
	1	0301037	US-PGPUB;	19:50
			EPO; JPO;	
	İ		IBM_TDB	
-	2	("5403775" "5872031").PN.	USPAT	2003/01/30
_	6	5937285.URPN.	USPAT	19:58
-	•	3937203.ORPN.	OSPAI	19:59
_	2	("5403775" "5872031").PN.	USPAT	2003/01/30
				20:04
-	6	("4530150" "4562638" "4590663"	USPAT	2003/01/31
		"4599789" "4637124" "4642878").PN.	HCDAM	15:49
_	6	("4366613" "4488351" "4505024" "4525920" "4530150" "4536944").PN.	USPAT	2003/01/31
_	58		USPAT;	2003/01/31
		transistor) and (deep\$4 with (source)))	US-PGPUB;	16:38
		_	EPO; JPO;	
			IBM_TDB	0000/01/01
-	35	<pre>(("NMOS" and "PMOS" and (second adj transistor) and (deep\$4 with (source))))</pre>	USPAT; US-PGPUB;	2003/01/31 15:56
		and thickness	EPO; JPO;	13.36
	İ		IBM TDB	
_	35	((("NMOS" and "PMOS" and (second adj	USPAT;	2003/01/31
		transistor) and (deep\$4 with (source))))	US-PGPUB;	15:58
		and thickness) and (diffus\$4 or deplet\$4	EPO; JPO;	
_	33	or dop\$4 or ion\$4) (((("NMOS" and "PMOS" and (second adj	IBM_TDB USPAT;	2003/01/31
		transistor) and (deep\$4 with (source))))	US-PGPUB;	15:59
		and thickness) and (diffus\$4 or deplet\$4	EPO; JPO;	
		or dop\$4 or ion\$4)) and etch\$4	IBM_TDB	
_	33	((((("NMOS" and "PMOS" and (second adj	USPAT;	2003/01/31
		transistor) and (deep\$4 with (source)))) and thickness) and (diffus\$4 or deplet\$4	US-PGPUB; EPO; JPO;	16:01
		or dop\$4 or ion\$4)) and etch\$4) and	IBM TDB	
		(method or process)	_	
-	10	(((((("NMOS" and "PMOS" and (second adj	USPAT;	2003/01/31
		transistor) and (deep\$4 with (source))))	US-PGPUB;	16:07
		and thickness) and (diffus\$4 or deplet\$4 or dop\$4 or ion\$4)) and etch\$4) and	EPO; JPO; IBM TDB	
		(method or process)) and (oxidi\$3)	1511_155	
-	0	(((((("NMOS" and "PMOS" and (second adj	USPAT;	2003/01/31
		transistor) and (deep\$4 with (source))))	US-PGPUB;	16:08
		and thickness) and (diffus\$4 or deplet\$4	EPO; JPO;	
		or dop\$4 or ion\$4)) and etch\$4) and (method or process)) and (oxidi\$3)) not	IBM_TDB	
		(((((("NMOS" and "PMOS" and (second adj		
		transistor) and (deep\$4 with (source))))		
		and thickness) and (diffus\$4 or deplet\$4		
		or dop\$4 or ion\$4)) and etch\$4) and (method or process))		
1_	23	(((((("NMOS" and "PMOS" and (second adj	USPAT;	2003/01/31
		transistor) and (deep\$4 with (source))))	US-PGPUB;	16:08
		and thickness) and (diffus\$4 or deplet\$4	EPO; JPO;	
		or dop\$4 or ion\$4)) and etch\$4) and	IBM_TDB	
		(method or process)) not (((((("NMOS"		
		and "PMOS" and (second adj transistor) and (deep\$4 with (source)))) and		
		thickness) and (diffus\$4 or deplet\$4 or		
		dop\$4 or ion\$4)) and etch\$4) and (method		
		or process)) and (oxidi\$3))		0000/01/05
-	23	(("NMOS" and "PMOS" and (second adj	USPAT;	2003/01/31
		transistor) and (deep\$4 with (source)))) not ((("NMOS" and "PMOS" and (second adj	US-PGPUB; EPO; JPO;	10:39
		transistor) and (deep\$4 with (source))))	IBM TDB	
		and thickness)		

Page 3

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"6054734").PN.) and (transistor and "PMOS" and "NMOS") ("NMOS" and "PMOS") USPAT; 2003/01/31 US-PGPUB; EPO; JPO; IBM_TDB USPAT; us-PGPUB; and "PMOS")) and (partial\$4 USPAT; us-PGPUB; EPO; JPO; IBM_TDB USPAT; us-PGPUB; EPO; JPO; IBM_TDB USPAT; us-PGPUB; EPO; JPO; IBM_TDB USPAT; us-PGPUB; EPO; JPO; IBM_TDB USPAT; us-PGPUB; IN IT: 40 user3 remov\$4)) and (source and drain) us-PGPUB; EPO; JPO; ISPO; ISPO; JPO; JPO; ISPO; JPO; JPO; ISPO; JPO; ISPO; JPO; JPO; JPO; JPO; JPO; JPO; JPO; J	1				1
- 18084 "PMOS" and "PMOS") - 247 (("NMOS" and "PMOS") USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; near3 remov\$4) - 206 ((("NMOS" and "PMOS")) and (partial\$4 USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; INTERPORT IN	1				
- 18084 ("NMOS" and "PMOS") - 247 (("NMOS" and "PMOS")) and (partial\$4 US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT;	1	1			
US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US	1				0000 (07 (07
- 247 (("NMOS" and "PMOS")) and (partial\$4 USPAT; 2003/01/31 near3 remov\$4) US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; 17:39 USPAT; 2003/01/31 near3 remov\$4)) and (partial\$4 uspAT; USPA	-	18084	("NMOS" and "PMOS")		1 ' '
- 247 (("NMOS" and "PMOS")) and (partial\$4 USPAT; 2003/01/31 near3 remov\$4)					17:38
- 247 (("NMOS" and "PMOS")) and (partial\$4 USPĀT; US-PGPUB; EPO; JPO; IBM_TDB USPĀT; US-PGPUB; EPO; JPO; IBM_TDB USPĀT; US-PGPUB; near3 remov\$4)) and (source and drain) USPĀT; US-PGPUB; EPO; JPO; EPO; JPO;		1			
near3 remov\$4) - 206 ((("NMOS" and "PMOS")) and (partial\$4	1				
- 206 ((("NMOS" and "PMOS")) and (partial\$4 USPAT; 2003/01/31 near3 remov\$4)) and (source and drain) US-PGPUB; EPO; JPO;	-	247			1
- 206 ((("NMOS" and "PMOS")) and (partial\$4 USPAT; 2003/01/31 near3 remov\$4)) and (source and drain) US-PGPUB; EPO; JPO;	1		near3 remov\$4)		17:39
- 206 ((("NMOS" and "PMOS")) and (partial\$4 USPAT; 2003/01/31 near3 remov\$4)) and (source and drain) US-PGPUB; 17:40 EPO; JPO;	1	1			
near3 remov\$4)) and (source and drain) US-PGPUB; 17:40 EPO; JPO;		1		IBM_TDB	
EPO; JPO;	-	206	((("NMOS" and "PMOS")) and (partial\$4	USPAT;	2003/01/31
EPO; JPO;			near3 remov\$4)) and (source and drain)	US-PGPUB;	17:40
TRM TDB			, · · · · · · · · · · · · · · · · · · ·	EPO; JPO;	1
				IBM TDB	

_	191	(((("NMOS" and "PMOS")) and (partial\$4	USPAT;	2003/01/31
1		near3 remov\$4)) and (source and drain))	US-PGPUB;	17:40
		and (insulat\$4 or dielectric)	EPO; JPO;	
			IBM_TDB	l
-	191	((((("NMOS" and "PMOS")) and (partial\$4	USPAT;	2003/01/31
		near3 remov\$4)) and (source and drain))	US-PGPUB;	17:41
		and (insulat\$4 or dielectric)) and	EPO; JPO;	
		(method or process)	IBM_TDB	
-	191	(((((("NMOS" and "PMOS")) and (partial\$4	USPAT;	2003/01/31
1		near3 remov\$4)) and (source and drain))	US-PGPUB;	17:42
,		and (insulat\$4 or dielectric)) and	EPO; JPO;	
		(method or process)) and substrate	IBM_TDB	
-	190	(((((("NMOS" and "PMOS")) and	USPAT;	2003/01/31
		(partial\$4 near3 remov\$4)) and (source	US-PGPUB;	17:44
		and drain)) and (insulat\$4 or	EPO; JPO;	
		dielectric)) and (method or process)) and	IBM_TDB	
		substrate) and (dop\$4 or depleti\$4 or ion]
		or implant\$4 diffus\$5)		
-	172	((((((("NMOS" and "PMOS")) and	USPAT;	2003/01/31
ļ		(partial\$4 near3 remov\$4)) and (source	US-PGPUB;	17:46
		and drain)) and (insulat\$4 or	EPO; JPO;	
1		dielectric)) and (method or process)) and	IBM_TDB	
1		substrate) and (dop\$4 or depleti\$4 or ion		
]		or implant\$4 diffus\$5)) and (doppant or		
		depletion or implantation or diffusion)		
-	18	(((((((("NMOS" and "PMOS")) and	USPAT;	2003/01/31
1		(partial\$4 near3 remov\$4)) and (source	US-PGPUB;	17:47
		and drain)) and (insulat\$4 or	EPO; JPO;	
	1	dielectric)) and (method or process)) and	IBM_TDB	
		substrate) and (dop\$4 or depleti\$4 or ion		
		or implant\$4 diffus\$5)) and (doppant or		
		depletion or implantation or diffusion))	1	
		and ((second adj region) or (second adj		
		portion))		
-	4		USPAT	2003/01/31
		"5754077").PN.		17:52
-	1	6060748.URPN.	USPAT	2003/01/31
				17:54
-	2777	("NMOS" and "PMOS" and (transistor with	USPAT;	2003/02/03
		(partial\$4 (etch\$4 or remov\$4))))	US-PGPUB;	14:50
			EPO; JPO;	
			IBM TDB	
-			_	0000/00/00
	3211	("NMOS" and "PMOS" and (substrate with	USPĀT;	2003/02/03
	3211	("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))	USPĀT; US-PGPUB;	2003/02/03 14:52
	3211		USPĀT; US-PGPUB; EPO; JPO;	
		(partial\$4 (etch\$4 or remov\$4))))	USPĀT; US-PGPUB; EPO; JPO; IBM_TDB	14:52
-	3211 581	<pre>(partial\$4 (etch\$4 or remov\$4)))) ("NMOS" and "PMOS" and ((silicon adj</pre>	USPĀT; US-PGPUB; EPO; JPO; IBM_TDB USPĀT;	2003/02/03
-		<pre>(partial\$4 (etch\$4 or remov\$4)))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB;	14:52
-		<pre>(partial\$4 (etch\$4 or remov\$4)))) ("NMOS" and "PMOS" and ((silicon adj</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/02/03
-	581	<pre>(partial\$4 (etch\$4 or remov\$4))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB	14:52 2003/02/03 14:52
-		<pre>(partial\$4 (etch\$4 or remov\$4)))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))) ((("NMOS" and "PMOS" and (substrate with</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT;	14:52 2003/02/03 14:52 2003/02/03
-	581	<pre>(partial\$4 (etch\$4 or remov\$4)))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))) ((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB;	14:52 2003/02/03 14:52
-	581	<pre>(partial\$4 (etch\$4 or remov\$4))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))) ((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	14:52 2003/02/03 14:52 2003/02/03
-	581	<pre>(partial\$4 (etch\$4 or remov\$4))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))) ((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB;	14:52 2003/02/03 14:52 2003/02/03
-	581 3284	<pre>(partial\$4 (etch\$4 or remov\$4))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))) ((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))))) and (method or process)</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB	14:52 2003/02/03 14:52 2003/02/03 14:54
-	581	<pre>(partial\$4 (etch\$4 or remov\$4))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))) ((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))))) and (method or process) (((("NMOS" and "PMOS" and (substrate with</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB	14:52 2003/02/03 14:52 2003/02/03 14:54
-	581 3284	<pre>(partial\$4 (etch\$4 or remov\$4))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))) ((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process) (((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))) or</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB;	14:52 2003/02/03 14:52 2003/02/03 14:54
-	581 3284	<pre>(partial\$4 (etch\$4 or remov\$4))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))) ((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process) (((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))) or (("NMOS" and "PMOS" and ((silicon adj</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	14:52 2003/02/03 14:52 2003/02/03 14:54
-	581 3284	<pre>(partial\$4 (etch\$4 or remov\$4))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))) ((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process) (((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB;	14:52 2003/02/03 14:52 2003/02/03 14:54
-	581 3284	<pre>(partial\$4 (etch\$4 or remov\$4))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))) ((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process) (((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))))) and (method or process))</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	14:52 2003/02/03 14:52 2003/02/03 14:54
-	581 3284 3008	<pre>(partial\$4 (etch\$4 or remov\$4))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))) ((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process) (((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process)) and (insulat\$4 or dielectric\$4)</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB	14:52 2003/02/03 14:52 2003/02/03 14:54 2003/02/03 14:55
-	581 3284	<pre>(partial\$4 (etch\$4 or remov\$4))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))) ((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process) (((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))))) and (method or process)) and (insulat\$4 or dielectric\$4) (((("NMOS" and "PMOS" and (substrate</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB	14:52 2003/02/03 14:52 2003/02/03 14:54 2003/02/03 14:55
-	581 3284 3008	<pre>(partial\$4 (etch\$4 or remov\$4))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))) ((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process) (((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process)) and (insulat\$4 or dielectric\$4) ((((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))))</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB	14:52 2003/02/03 14:52 2003/02/03 14:54 2003/02/03 14:55
-	581 3284 3008	<pre>(partial\$4 (etch\$4 or remov\$4))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))) ((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process) (((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))))) and (method or process)) and (insulat\$4 or dielectric\$4) ((((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))) or (("NMOS" and "PMOS" and (silicon adj or ("NMOS" and "PMOS" and (silicon adj or ("NMOS" and "PMOS" and (silicon adj</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB	14:52 2003/02/03 14:52 2003/02/03 14:54 2003/02/03 14:55
-	581 3284 3008	<pre>(partial\$4 (etch\$4 or remov\$4))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))) ((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process) (((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process)) and (insulat\$4 or dielectric\$4) ((((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))) or (("NMOS" and "PMOS" and (silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB	14:52 2003/02/03 14:52 2003/02/03 14:54 2003/02/03 14:55
-	581 3284 3008	<pre>(partial\$4 (etch\$4 or remov\$4))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))) ((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process) (((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))))) and (method or process)) and (insulat\$4 or dielectric\$4) (((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and (silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))))) and (method or process))</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB	14:52 2003/02/03 14:52 2003/02/03 14:54 2003/02/03 14:55
-	581 3284 3008	<pre>(partial\$4 (etch\$4 or remov\$4))) ("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4)))) ((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process) (((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process)) and (insulat\$4 or dielectric\$4) ((((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))) or (("NMOS" and "PMOS" and (silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB	14:52 2003/02/03 14:52 2003/02/03 14:54 2003/02/03 14:55

with (partial34 (etch44 or remov44)))) or (("MMOS" and "PMOS" and (silicon ad) layer) with (partial34 (etch44 or process) and (insular44 or dielectric44)) and and (insular44 or dielectric44)) and with (partial34 (etch44 or process) with (partial34 (etch44 or process) with (partial34 (etch44 or process)) with (partial34 (etch44 or process)) with (partial44 (etch44 or process)) with (p		r			
remov94))))) and (method or process) and (insulat84 or dielectric54)) and (oxidis3)) and (second adj transistor) (insulat84 or dielectric54)) and (subtrate with (partial84 (etch84 or remov94)))) or (("NMOS" and "PMOS" and (subtrate with (partial84 (etch84 or remov94)))) and (source and drain and gate) (("("MOS" and "PMOS" and (subtrate with (partial84 (etch84 or remov94))))) or (("MOS" and "PMOS" and (subtrate with (partial84 (etch84 or remov94))))) or (("MOS" and "PMOS" and (subtrate with (partial84 (etch84 or remov94))))) or (("MOS" and "PMOS" and (subtrate with (partial84 (etch84 or remov94))))) or (("MOS" and "PMOS" and (subtrate with (partial84 (etch84 or remov94))))) or (("MOS" and "PMOS" and (subtrate with (partial84 (etch84 or remov94))))) or (("MOS" and "PMOS" and (subtrate with (partial84 (etch84 or remov94))))) or (("MOS" and "PMOS" and (subtrate with (partial84 (etch84 or remov94))))) and (method or process)) and (oxidis3)) and (second adj transistor)) and (source and drain and gate)) and (sucre and drain and gate)) and (source and drain and gate)) and (sucre and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate) and (source and source and drain and gate) and (source and source a	-	92	or (("NMOS" and "PMOS" and ((silicon adj	EPO; JPO;	2003/02/03 14:56
S80 (((((((((((((((((((((((((((((((((((remov\$4)))))) and (method or process)) and (insulat\$4 or dielectric\$4)) and	13.1_133	
and (source and drain and gate) ((((((((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))) or (("NMOS" and "MOS" and (solicon ad) layer) with (partial\$4 (etch\$4 or remov\$4)))) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (source and drain and gate)) and (((((((((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))) and (source and drain and gate)) and ((oxidi\$3)) and (second and partial\$4 or dielectric\$4)) and (((((((((((((((((((((((((((((((((((-	80	<pre>with (partial\$4 (etch\$4 or remov\$4)))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process)) and (insulat\$4 or dielectric\$4)) and</pre>	US-PGPUB; EPO; JPO;	
layer with (partial\$4 (etch\$4 or removs\$4))))) and (method or process)) and (insulat\$4 or dielectrics\$4)) and (cxidi\$3)) and (second adj portion)	-	15	<pre>and (source and drain and gate) ((((((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4)))))</pre>	US-PGPUB;	1 ' '
Section			layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process)) and (insulat\$4 or dielectric\$4)) and (oxidi\$3)) and (second adj transistor)) and (source and drain and gate)) and		
(oxidi\$3)) and (second adj transistor)) and (source and drain and qate)) not (((((((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))) or (("NMOS" and "PMOS" and (silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (source and drain and (solicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (insulat\$4 or dielectric\$4) and (oxidi\$3)) and (second adj transistor)) and (source and drain and gate)) and (second adj portion)) 1 "20020179957" 2003/04/30 USPAT; US-PCPUB; EPG; JPG; IBM TDB USPĀT 17:55 8 "5111255" USPAT;	-	65	<pre>(((((((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))) or (("NMOS" and "PMOS" and ((silicon adj layer) with (partial\$4 (etch\$4 or remov\$4))))) and (method or process))</pre>	US-PGPUB; EPO; JPO;	
remov\$4)))) and (method or process)) and (insulat\$4 or dielectric\$4)) and (oxidi\$3)) and (second adj transistor)) and (source and drain and gate)) and (second adj portion)) 1 "20020179957" 2003/04/30 USPAT; USP			<pre>(oxidi\$3)) and (second adj transistor)) and (source and drain and gate)) not (((((((("NMOS" and "PMOS" and (substrate with (partial\$4 (etch\$4 or remov\$4))))) or (("NMOS" and "PMOS" and ((silicon adj</pre>		
			remov\$4)))))) and (method or process)) and (insulat\$4 or dielectric\$4)) and (oxidi\$3)) and (second adj transistor)) and (source and drain and gate)) and (second adj portion))		
- 0 20020179957.URPN. USPĀT 2003/04/29 17:55 - 8 "5111255" USPAT; USPAT; USPĀT 10:02 - 8 ("4558337" "4590502" "4686550" USPĀT 2003/04/30 10:02 - 8 ("4800415" "4806997" "4814836" "486068" "4862228").PN. USPĀT 2003/04/30 10:03 - 7 5111255.URPN. USPĀT 2003/04/30 10:04 - 3 ("4825264" "5111255" "5162877").PN. USPĀT 2003/04/30 14:48 - 8 ("4558337" "4590502" "4686550" USPĀT 2003/04/30 14:49 - 8 ("4558337" "4590502" "4686550" USPĀT 2003/04/30 14:49 - 7 5111255.URPN. USPĀT 2003/04/30 14:49 - 7 5111255.URPN. USPĀT 2003/04/30 14:49 - 4840068" "4862228").PN. USPĀT 2003/04/30 14:50 - 523 ((semiconductor adj layer) with etch\$4) USPĀT; USPĢUB; EPO; JPO; IBM_TDB	_	1	"20020179957" 	US-PGPUB; EPO; JPO;	
S	-	0	20020179957.URPN.	USPĀT	1 ' '
- 8 ("4558337" "4590502" "4686550" USPĀT 2003/04/30 10:03	-	8	"5111255"	US-PGPUB; EPO; JPO;	
- 7 5111255.URPN. USPAT 2003/04/30 10:04 - 3 ("4825264" "5111255" "5162877").PN. USPAT 2003/04/30 14:48 - 8 ("4558337" "4590502" "4686550" USPAT 2003/04/30 14:49 - 4860068" "4860228").PN. USPAT 2003/04/30 14:49 - 523 ((semiconductor adj layer) with etch\$4) and (isolation adj region) USPAT; 2003/05/05 18M TDB 2003/05/05 and (isolation adj region) uspat; 2003/05/05 18M TDB 2003/0	-	8	"4800415" "4806997" "4814836"		1
- 8 ("4558337" "4590502" "4686550" USPAT 2003/04/30	-	7		USPAT	
"4800415" "4806997" "4814836" "4860068" "4862228").PN. 7 5111255.URPN. USPAT 2003/04/30 14:50 2003/05/05 and (isolation adj region) USPAT;	-	3	("4825264" "5111255" "5162877").PN.	USPAT	
- 5111255.URPN. USPAT 2003/04/30 14:50 ((semiconductor adj layer) with etch\$4) USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; and (isolation adj region) uspAT; uspAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; and (isolation adj region)) and (isolation adj region)) and (insulat\$4) USPAT; US-PGPUB; EPO; JPO;	-	8	"4800415" "4806997" "4814836"	USPAT	
and (isolation adj region) - 484 (((semiconductor adj layer) with etch\$4) and (isolation adj region)) and (insulat\$4) US-PGPUB; EPO; JPO; US-PGPUB; US-PGPUB; EPO; JPO;	_	7		USPAT	14:50
- 484 (((semiconductor adj layer) with etch\$4) USPAT; 2003/05/05 and (isolation adj region)) and US-PGPUB; 01:44 EPO; JPO;	-	523		US-PGPUB; EPO; JPO;	
IBM TDB	-	484	and (isolation adj region)) and	USPĀT; US-PGPUB;	

		<u> </u>		
-	66	((((semiconductor adj layer) with etch\$4)	USPAT;	2003/05/05
1		and (isolation adj region)) and	US-PGPUB;	01:45
		(insulat\$4)) and (insulati\$4 adj substrate)	EPO; JPO; IBM TDB	
	59	Substrace; (((((semiconductor adj layer) with	USPAT;	2003/05/05
-	1 39	etch\$4) and (isolation adj region)) and	US-PGPUB;	01:45
1		(insulat\$4)) and (insulati\$4 adj	EPO; JPO;	01.13
		substrate)) and transistor	IBM TDB	
_	40	((((((semiconductor adj layer) with	USPAT;	2003/05/05
		etch\$4) and (isolation adj region)) and	US-PGPUB;	01:45
		(insulat\$4)) and (insulati\$4 adj	EPO; JPO;	
	!	substrate)) and transistor) and oxidiz\$4	IBM TDB	
_	39		USPĀT;	2003/05/05
		etch\$4) and (isolation adj region)) and	US-PGPUB;	01:46
		(insulat\$4)) and (insulati\$4 adj	EPO; JPO;	
		substrate)) and transistor) and oxidiz\$4)	IBM_TDB	
		and (mask)		
-	0	((((((((semiconductor adj layer) with	USPAT;	2003/05/05
		etch\$4) and (isolation adj region)) and	US-PGPUB;	01:47
		(insulat\$4)) and (insulati\$4 adj	EPO; JPO;	
		substrate)) and transistor) and oxidiz\$4)	IBM_TDB	
	_	and (mask)) and resist) and antireflect\$4	Henry.	2002/05/05
-	0		USPAT;	2003/05/05 01:47
1		etch\$4) and (isolation adj region)) and	US-PGPUB; EPO; JPO;	01:47
		<pre>(insulat\$4)) and (insulati\$4 adj substrate)) and transistor) and oxidiz\$4)</pre>	IBM TDB	
		and (mask)) and resist) and (anti adj	TBM_TDB	
		reflect\$4)		
1_	29		USPAT;	2003/05/05
	23	etch\$4) and (isolation adj region)) and	US-PGPUB;	02:02
		(insulat\$4)) and (insulati\$4 adj	EPO; JPO;	
		substrate)) and transistor) and oxidiz\$4)	IBM TDB	
		and (mask)) and resist	_ · · ·	
_	14	("4314267" "4609931" "4612629"	USPAT	2003/05/05
	}	"4672416" "4830973" "4939563"		01:57
		"4947192" "5034782" "5040041"		
		"5075250" "5140400" "5177584"		
		"5412240" "5434441").PN.		
-	10		USPAT;	2003/05/05
		etch\$4) and (isolation adj region)) and	US-PGPUB;	02:04
		(insulat\$4)) and (insulati\$4 adj	EPO; JPO;	
		substrate)) and transistor) and oxidiz\$4)	IBM_TDB	
		and (mask)) not (((((((semiconductor		
		adj layer) with etch\$4) and (isolation adj region)) and (insulat\$4)) and		
		(insulati\$4 adj substrate)) and		
		transistor) and oxidiz\$4) and (mask)) and		
		resist)		
_	127	1	USPAT;	2003/05/05
		and (isolation adj region)) and	US-PGPUB;	02:10
		(insulat\$4)) and (lower adj (portion or	EPO; JPO;	
		region or section or area))	IBM_TDB	
_	112	(((((semiconductor adj layer) with	USPAT;	2003/05/05
	1	etch\$4) and (isolation adj region)) and	US-PGPUB;	02:06
	1	(insulat\$4)) and (lower adj (portion or	EPO; JPO;	
1		region or section or area))) and	IBM_TDB	
		(insulat\$4 adj layer)		
_	112	((((((semiconductor adj layer) with	USPAT;	2003/05/05
		etch\$4) and (isolation adj region)) and	US-PGPUB;	02:06
	[(insulat\$4)) and (lower adj (portion or	EPO; JPO;	
		region or section or area))) and	IBM_TDB	
1	1	(insulat\$4 adj layer)) and (substrate and		
1		etch\$4)	IISDAT.	2003/05/05
-	0	<pre>(((((((semiconductor adj layer) with etch\$4) and (isolation adj region)) and</pre>	USPAT; US-PGPUB;	02:07
	1	(insulat\$4)) and (lower adj (portion or	EPO; JPO;	02.07
	1	region or section or area))) and	IBM TDB	
		(insulat\$4 adj layer)) and (substrate and	10.1_100	
		etch\$4)) and (mask and resist and		
		oxidez\$4)		
L .		<u></u>	<u> </u>	

_	26	<pre>(((((((semiconductor adj layer) with etch\$4) and (isolation adj region)) and (insulat\$4)) and (lower adj (portion or region or section or area))) and</pre>	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2003/05/05 02:07
	2.1	<pre>(insulat\$4 adj layer)) and (substrate and etch\$4)) and (mask and resist)</pre>	-	2003/05/05
_	21	<pre>((((((((semiconductor adj layer) with etch\$4) and (isolation adj region)) and (insulat\$4)) and (lower adj (portion or region or section or area))) and (insulat\$4 adj layer)) and (substrate and etch\$4)) and (mask and resist)) not ((((((semiconductor adj layer) with etch\$4) and (isolation adj region)) and (insulat\$4)) and (insulati\$4 adj substrate)) and transistor) and oxidiz\$4) and (mask))</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	02:07
-	12	<pre>(((((semiconductor adj layer) with etch\$4) and (isolation adj region)) and (insulat\$4)) and (insulati\$4 adj substrate)) and (lower adj (portion or region or section or area))</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/05/05 02:10
_	11	<pre>((((((semiconductor adj layer) with etch\$4) and (isolation adj region)) and (insulat\$4)) and (insulati\$4 adj substrate)) and (lower adj (portion or region or section or area))) not ((((((((semiconductor adj layer) with etch\$4) and (isolation adj region)) and</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/05/05 02:11
		<pre>(insulat\$4)) and (lower adj (portion or region or section or area))) and (insulat\$4 adj layer)) and (substrate and etch\$4)) and (mask and resist)) not (((((((semiconductor adj layer) with etch\$4) and (isolation adj region)) and (insulat\$4)) and (insulati\$4 adj substrate)) and transistor) and oxidiz\$4) and (mask)))</pre>		
	11	<pre>(((((((semiconductor adj layer) with etch\$4) and (isolation adj region)) and (insulat\$4)) and (insulati\$4 adj substrate)) and (lower adj (portion or region or section or area))) not ((((((((semiconductor adj layer) with etch\$4) and (isolation adj region)) and (insulat\$4)) and (lower adj (portion or region or section or area))) and (insulat\$4 adj layer)) and (substrate and etch\$4)) and (mask and resist)) not (((((((semiconductor adj layer) with etch\$4)) and (isolation adj region)) and (insulat\$4)) and (insulati\$4 adj substrate)) and transistor) and oxidiz\$4) and (mask)))) not ((((((((semiconductor adj layer) with etch\$4) and (isolation adj region)) and (insulat\$4)) and (insulati\$4 adj substrate)) and transistor) and oxidiz\$4) and</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/05/05 02:13
-	523	resist) and antireflect\$4) (((semiconductor adj layer) with etch\$4) and (isolation adj region)) and ((semiconductor adj layer) same etch\$4)	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2003/05/05 02:14
-	523	(((semiconductor adj layer) with etch\$4) and (isolation adj region)) and ((semiconductor adj layer) with etch\$4)	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2003/05/05 02:14

Page 8

	27	1/1/1	TTC D D III .	2002/05/05
-	27	(((((semiconductor adj layer) with	USPAT; US-PGPUB;	2003/05/05 02:21
		etch\$4) and (isolation adj region)) and (insulat\$4)) and (insulati\$4 adj	EPO; JPO;	02:21
		substrate)) not (((((((semiconductor adj	IBM TDB	
		layer) with etch\$4) and (isolation adj	1011_100	
		region)) and (insulat\$4)) and (insulati\$4		
		adj substrate)) and transistor) and		
		oxidiz\$4) and (mask))		
_	4	(((semiconductor adj layer) with etch\$4)	USPAT;	2003/05/05
		and (isolation adj region)) and (lower	US-PGPUB;	02:21
		adj transistor)	EPO; JPO;	
		_ aag	IBM TDB	
_	7	("5623155" "5729039" "5973364"	USPĀT	2003/05/05
		"6191449" "6288425" "6348714"		02:22
		"6414355" "2002/0048972").PN.		
-	592	substrate and insulat\$4 and (isolati\$3	USPAT;	2003/05/05
		adj region) and (second adj transistor)	US-PGPUB;	02:57
			EPO; JPO;	
			IBM_TDB	
-	86	(substrate and insulat\$4 and (isolati\$3	USPAT;	2003/05/05
		adj region) and (second adj transistor))	US-PGPUB;	02:26
		and (mask and resist and oxidiz\$4)	EPO; JPO;	
			IBM_TDB	0000 (05 (55
-	85	((substrate and insulat\$4 and (isolati\$3	USPAT;	2003/05/05
		adj region) and (second adj transistor))	US-PGPUB;	02:28
1		and (mask and resist and oxidiz\$4)) and	EPO; JPO;	
		etch\$4	IBM_TDB USPAT;	2003/05/05
-	80	(((substrate and insulat\$4 and (isolati\$3		
	:	adj region) and (second adj transistor))	US-PGPUB;	02:28
		and (mask and resist and oxidiz\$4)) and	EPO; JPO;	
		etch\$4) not (((((semiconductor adj layer)	IBM_TDB	
		with etch\$4) and (isolation adj region))		
		<pre>and (insulat\$4)) and (insulati\$4 adj substrate))</pre>		
	0	20020125512.URPN.	USPAT	2003/05/05
-	0	20020123312.0RFN.	OSTAI	02:34
	10	(substrate and insulat\$4 and (isolati\$3	USPAT;	2003/05/05
	•	adj region) and (second adj transistor))	US-PGPUB;	03:00
		and (lower adj transistor)	EPO; JPO;	
			IBM TDB	
_	713	("silicon-on-insulator" or "SOI") and	USPAT;	2003/05/05
		(isolat\$4 adj trench)	US-PGPUB;	03:01
			EPO; JPO;	
			IBM_TDB	
-	3	"6037632"	USPĀT;	2003/05/05
			US-PGPUB;	10:32
			EPO; JPO;	
	_		IBM_TDB	2002/05/05
-	7	("4422089" "5132769" "5177572"	USPAT	2003/05/05
		"5438215" "5488236" "5510634"		10:34
	_	"5572048").PN.	USPAT	2003/05/05
-	2	6037632.URPN.	USPAI	10:36
_	0	("535" and (second adj transistor)) and	USPAT;	2003/05/05
-		(fully adj depleted adj transistor)	US-PGPUB;	10:41
		(rurry adj depreced adj cransistor)	EPO; JPO;	
			IBM TDB	
_	0	("535" and (second adj transistor)) and	USPAT;	2003/05/05
	1	(partially adj depleted adj transistor)	US-PGPUB;	10:42
1	1	'English and an english and english and an english and an english and an english and an english and an english and an english and an english and an english	EPO; JPO;	
			IBM TDB	
-	123	("535" and (second adj transistor)) and	USPAT;	2003/05/05
		("NMOS" and "PMOS")	US-PGPUB;	10:42
			EPO; JPO;	
			IBM_TDB	
-	11		USPĀT;	2003/05/05
		("NMOS" and "PMOS")) and (mask or resisk	US-PGPUB;	10:43
		or photoresist)	EPO; JPO;	
			IBM_TDB	<u> </u>

-	11	(("535" and (second adj transistor)) and	USPAT;	2003/05/05
		("NMOS" and "PMOS")) and (mask or resist	US-PGPUB;	10:47
		or photoresist)	EPO; JPO;	
			IBM_TDB	/ /
-	486	"535" and (second adj transistor)	USPĀT;	2003/05/05
			US-PGPUB;	11:04
			EPO; JPO;	
			IBM_TDB	
-	535	((("silicon-on-insulator" or "SOI") and	USPAT;	2003/05/05
		(isolat\$4 adj trench)) and transistor and	US-PGPUB;	11:08
		etch\$4) and insulat\$3	EPO; JPO;	
			IBM_TDB	((
-	0	((("535" and (second adj transistor)) and	USPAT;	2003/05/05
		("NMOS" and "PMOS")) not ((("535" and	US-PGPUB;	11:15
		(second adj transistor)) and ("NMOS" and	EPO; JPO;	
1		"PMOS")) and (mask or resisk or	IBM_TDB	
		photoresist))) and (isolati\$4 adj trench)		0000/05/05
-	0	((("535" and (second adj transistor)) and	USPAT;	2003/05/05
		("NMOS" and "PMOS")) not ((("535" and	US-PGPUB;	11:16
		(second adj transistor)) and ("NMOS" and	EPO; JPO;	
		"PMOS")) and (mask or resisk or	IBM_TDB	į į
		photoresist))) and (isolat\$4 adj trench)	ant =	2002/05/05
-	112	(("535" and (second adj transistor)) and	USPAT;	2003/05/05
		("NMOS" and "PMOS")) not ((("535" and	US-PGPUB;	11:51
ŀ		(second adj transistor)) and ("NMOS" and	EPO; JPO;	
		"PMOS")) and (mask or resisk or	IBM_TDB	
		photoresist))		0000/05/05
-	0	((("535" and (second adj transistor)) and	USPAT;	2003/05/05
		("NMOS" and "PMOS")) not ((("535" and	US-PGPUB;	11:17
		(second adj transistor)) and ("NMOS" and	EPO; JPO;	
		"PMOS")) and (mask or resisk or	IBM_TDB	
		photoresist))) and (isolat\$4 adj trench)	******	2002/05/05
-	112	((("535" and (second adj transistor)) and	USPAT;	2003/05/05
		("NMOS" and "PMOS")) not ((("535" and	US-PGPUB;	11:17
ľ		(second adj transistor)) and ("NMOS" and	EPO; JPO;	
		"PMOS")) and (mask or resisk or	IBM_TDB	
		photoresist))) and transistor	IIGDAM.	2002/05/05
-	0	1 () () + + +	USPAT;	2003/05/05
		and ("NMOS" and "PMOS")) not ((("535" and	US-PGPUB;	11:18
		(second adj transistor)) and ("NMOS" and	EPO; JPO;	
		"PMOS")) and (mask or resisk or	IBM_TDB	
		photoresist))) and transistor) and	:	
		(isolat\$4 adj trench)	HCDAT.	2003/05/05
-	0		USPAT; US-PGPUB;	11:18
		and ("NMOS" and "PMOS")) not ((("535" and	EPO; JPO;	11:10
		(second adj transistor)) and ("NMOS" and		
1		"PMOS")) and (mask or resisk or	IBM_TDB	
1		photoresist))) and transistor) and		
1_	0	(isolati\$3 adj trench) (((("535" and (second adj transistor))	USPAT;	2003/05/05
1		and ("NMOS" and "PMOS")) not ((("535" and	US-PGPUB;	11:18
			EPO; JPO;	11.10
1		(second adj transistor)) and ("NMOS" and "PMOS")) and (mask or resisk or	IBM TDB	
			TELL_IDE	
		photoresist))) and transistor) and		
1	403	(trench)	יייגם איי.	2003/05/05
-	483	(((("silicon-on-insulator" or "SOI") and	USPAT; US-PGPUB;	11:18
		(isolat\$4 adj trench)) and transistor and etch\$4) and insulat\$3) and (isolati\$4 adj	EPO; JPO;	11.10
1		trench)	IBM TDB	
	65	,,	USPAT;	2003/05/05
1 -	65	((((("silicon-on-insulator" or "SOI") and	US-PGPUB;	11:50
		(isolat\$4 adj trench)) and transistor and	EPO; JPO;	11.50
		etch\$4) and insulat\$3) and (isolati\$4 adj trench)) and (("PMOS" and "NMOS") and	IBM TDB	
1			T PIN _ I D D	
	9	(isolat\$4 adj trench))	USPAT	2003/05/05
-		("4090254" "4276616" "4677589" "4933899" "5027323" "5038191"	USPAI	11:36
				11.30
	10	"5060194" "5757693" "5761114").PN.	USPAT	2003/05/05
-	19	5909400.URPN.	USPAI	11:39
		("5898196" "5909400").PN.	USPAT	2003/05/05
_	2	(5090190" 5909400").PN.	USPAI	11:42
Í	l			11.76

		6180986.URPN.	USPAT	2003/05/05
-	0	6180986. ORPN.	USPAT	11:43
-	19	5909400.URPN.	USPAT	2003/05/05 11:44
_	112	<pre>(((("535" and (second adj transistor)) and ("NMOS" and "PMOS")) not ((("535" and (second adj transistor)) and ("NMOS" and "PMOS")) and (mask or resisk or photoresist))) and transistor) not (((((("silicon-on-insulator" or "SOI") and (isolat\$4 adj trench)) and transistor and etch\$4) and insulat\$3) and (isolat\$4 adj trench)) and (("PMOS" and "NMOS") and (isolat\$4 adj trench)))</pre>	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/05/05 11:51
-	112	(("535" and (second adj transistor)) and ("NMOS" and "PMOS")) not ((("535" and (second adj transistor)) and ("NMOS" and "PMOS")) and (mask or resisk or photoresist))) not (((((("silicon-on-insulator" or "SOI") and (isolat\$4 adj trench)) and transistor and etch\$4) and insulat\$3) and (isolat\$4 adj trench)) and (isolat\$4 ("PMOS" and "NMOS") and (isolat\$4 adj trench))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/05/05 12:01
-	20	"5909400"	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2003/05/05 15:17
_	2	("5898196" "5909400").PN.	USPAT	2003/05/05 12:03
-	2	(("5,909,400") or ("20010045561")).PN.	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2003/05/05 15:17
-	11	"5940691"	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2003/08/21 15:00
-	19	"5463238"	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/08/21 15:01
-	30	"5940691" or "5463238"	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/08/21 15:01
_	7	("3783052" "4725875" "4753896" "4816893" "4819052" "5081062" "5164803").PN.	USPĀT	2003/08/21 15:12
_	3		USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/08/26 11:05
-	41	en-william-g.in.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/11/21 14:38
-	10	pellerin-john-g.in.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/11/21 14:38
-	110	michael-mark-w.in.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/11/21
-	0	mark-michael-w.in.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/11/21 14:39

			TICDAM.	1 2002 /11 /21
-	157	en-william-g.in. pellerin-john-g.in. michael-mark-w.in.	USPAT; US-PGPUB;	2003/11/21 14:39
		michael-mark-w.in.	EPO; JPO;	14:39
			IBM TDB	
1_	100	en-william-g.in. pellerin-john-g.in.	USPAT;	2003/11/21
		michael-mark-w.in.) and source and drain	US-PGPUB;	14:39
			EPO; JPO;	
			IBM TDB	
-	98	((en-william-g.in. pellerin-john-g.in.	USPAT;	2003/11/21
	i	michael-mark-w.in.) and source and drain)	US-PGPUB;	14:40
		and (second transistor)	EPO; JPO;	
	_		IBM_TDB	
-	5	((en-william-g.in. pellerin-john-g.in.	USPAT;	2003/11/21
		michael-mark-w.in.) and source and drain)	US-PGPUB; EPO; JPO;	14:42
·		and (second adj transistor)	IBM TDB	
	10	((en-william-g.in. pellerin-john-g.in.	USPAT;	2003/11/21
-	10	michael-mark-w.in.) and source and drain)	US-PGPUB;	14:42
		and (second adj source)	EPO; JPO;	1 1 1 1 2
		and (become day boarco)	IBM TDB	
-	9	(((en-william-g.in. pellerin-john-g.in.	USPAT;	2004/03/09
		michael-mark-w.in.) and source and drain)	US-PGPUB;	16:13
		and (second adj source)) not	EPO; JPO;	
		(((en-william-g.in. pellerin-john-g.in.	IBM_TDB	
,		michael-mark-w.in.) and source and drain)		
		and (second adj transistor))		
-	410	(second adj transistor) and (buried adj	USPAT;	2003/11/21
		layer)	US-PGPUB;	14:44
		·	EPO; JPO; IBM TDB	
1_	410	((second adj transistor) and (buried adj	USPAT;	2003/11/21
	110	layer)) not (en-william-g.in.	US-PGPUB;	14:44
	1	pellerin-john-g.in. michael-mark-w.in.)	EPO; JPO;	
			IBM TDB]
-	31	(((second adj transistor) and (buried adj	USPAT;	2003/11/21
		layer)) not (en-william-g.in.	US-PGPUB;	14:44
		pellerin-john-g.in. michael-mark-w.in.))	EPO; JPO;	
		and (second adj source)	IBM_TDB	0000/11/01
-	33	(US-6440832-\$ or US-5963278-\$ or	USPAT;	2003/11/21
		US-5899547-\$ or US-5937285-\$ or	US-PGPUB	15:03
		US-6100124-\$ or US-5908309-\$ or US-5273914-\$ or US-5015595-\$ or		
		US-4477310-\$ or US-4902634-\$ or		
		US-4703551-\$ or US-4771014-\$ or		
		US-4422885-\$ or US-6501097-\$ or		
		US-4637124-\$ or US-4642878-\$ or		
		US-6235568-\$ or US-6060748-\$ or		
		US-5627399-\$ or US-6420730-\$ or		
1		US-5834350-\$ or US-5428224-\$ or		
	İ	US-5162877-\$ or US-5909400-\$ or		
		US-6180986-\$ or US-6537891-\$).did. or		
		(US-5463238-\$ or US-5164803-\$).did. or (US-20020195623-\$ or US-20010016377-\$ or		
]	US-20020130393-\$ or US-20010016377-\$ or US-20020102780-\$ or		
		US-20020171077-\$).did.		
_	20	1	USPAT	2003/11/21
		"5488579" "5594371" "5670387"		15:19
		"5670388" "5767549" "5789286"		
		"5811855" "5821769" "5877046"		
		"5929476" "5940691" "5985683"		
		"5985728" "6054345" "6061268"		
		"6084259" "6144072").PN.	IICDAM.	2002/11/21
-	0	6537891.URPN.	USPAT	2003/11/21
1_	0	6537891.URPN.	USPAT	2003/11/21
		000 / 00 / 1. ONE IV.		15:21